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	APPLICANT David Meltzer, et al.	
	FILING DATE March 8, 2004	GROUP

**U.S. PATENT DOCUMENTS**

E.I.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	AA					
	AB					
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	AJ					
	AK					

**FOREIGN PATENT DOCUMENTS**

E.I.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
	AL					
	AM					
	AN					
	AO					

**OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)**

JATM	AP	F.M. Gardner, "Properties of Frequency Difference Detectors" IEEE Transactions on Communications Vol. COM -33, No. 2, February 1985, pp 131-138
JATM	AQ	B. Razavi, "Design of Monolithic Phase-Locked Loops and Clock Recovery Circuits - A Tutorial" in Monolithic Phase-Locked Loops and Clock Recovery Circuits, Edited by Behzad Razavi, New York, IEEE Press, 1996
JATM	AR	I. Hwang, S. Song, S. Kim, "A Digitally Controlled Phase-Locked Loop With a Digital Phase-Frequency Detector for Fast Acquisition", IEEE Journal of Solid-State Circuits, Vol. 36, No. 10, October 2001, PP 1574-1581
JATM	AS	A. Hajimiri and T.H. Lee, "Design Issues in CMOS Differential LC Oscillators", IEEE Journal Of Solid-State Circuits, Vol. 34, No. 5, May 1999, pp 717-724
EXAMINER /Juan Torres/		DATE CONSIDERED 03/22/2007

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.